## NOTICE

- 1. Document Verification for successful candidates of Junior Engineer Examination 2016 would be conducted by the concerned Regional Offices of the Commission in consonance with the schedule decided by them, between 23<sup>rd</sup> January 2017 and 3<sup>rd</sup> February 2017. The specific schedule of document verification are being hosted by the concerned Regional Offices on their respective websites separately.
- 2. The Skill Tests for Data Entry Operators and LDCs etc. of the successful candidates of Combined Higher Secondary Level Examination-2015 (Tier-II) would be conducted by the Regional Offices of the Commission in consonance with the schedule decided by them between 11<sup>th</sup> February 2017 and 11<sup>th</sup> March 2017. The specific schedule of Skill Tests would be hosted by the concerned Regional Offices on their respective websites separately.
- 3. The Commission proposes to conduct Combined Graduate Level Examination-2016 (Tier-III) tentatively on 5<sup>th</sup> March 2017. The firm date of the examination, upon its finalisation, would be notified shortly.
- 4. The Commission proposes to conduct Junior Engineer 2016 Examination tentatively from 1<sup>st</sup> March 2017 to 4<sup>th</sup> March 2017. On finalisation of the firm dates for the said examination, the Notification to that effect would be hosted on the websites of the Commission in due course.

Secretary-cum-CoE